

FIG. 1

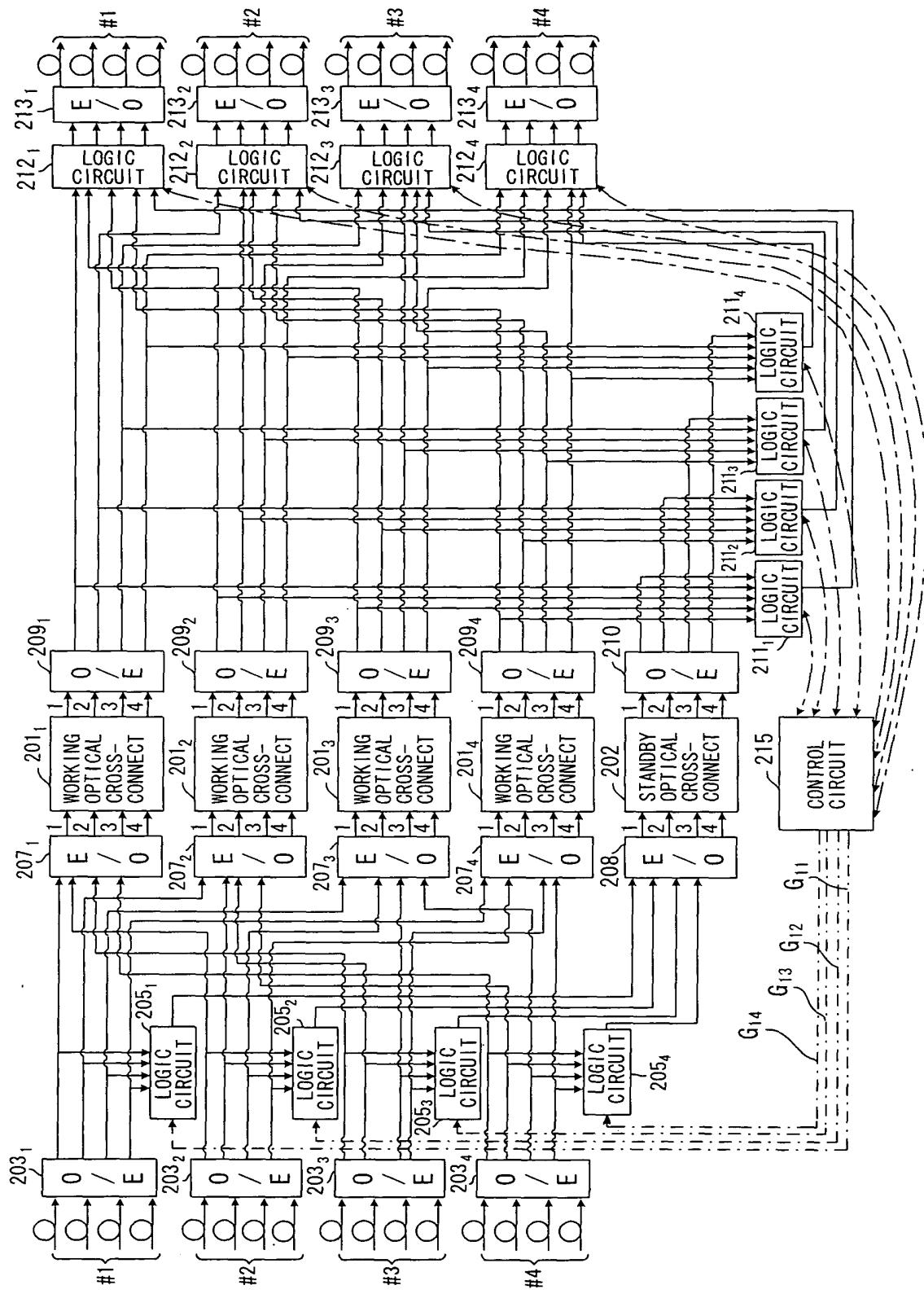


FIG. 2

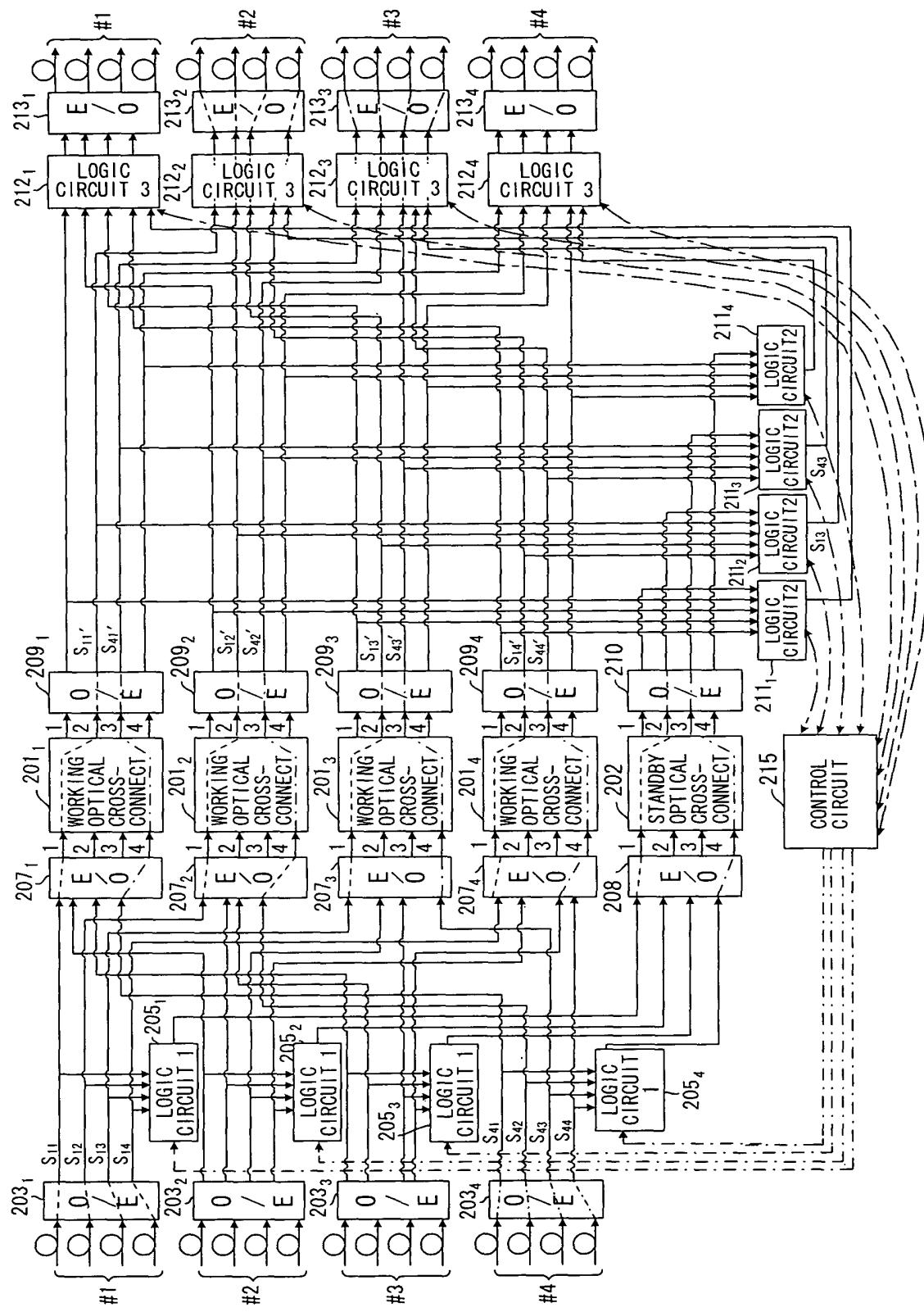


FIG. 3

(a) WHEN OPERATION IS NORMAL

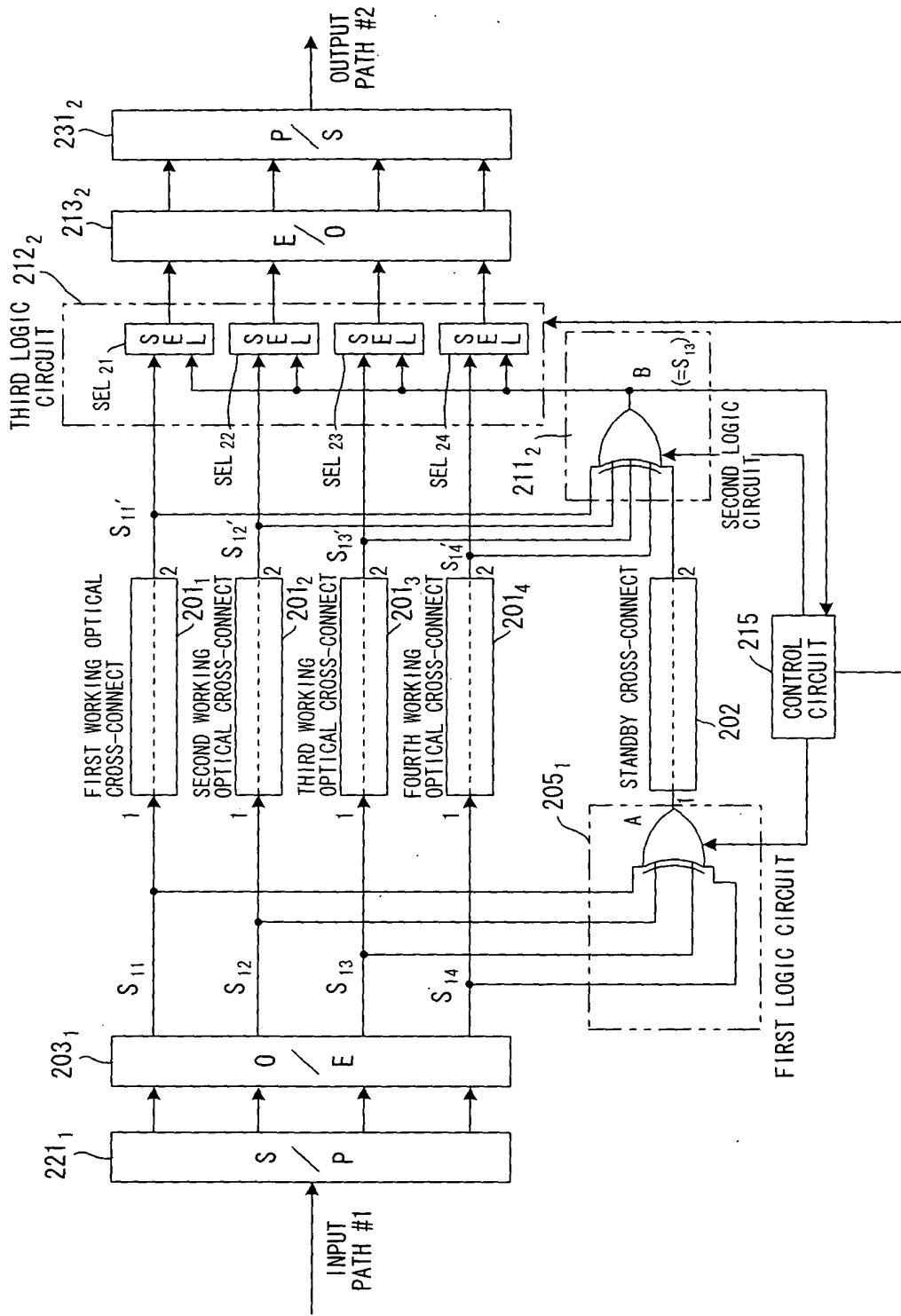
	NUMBER OF "1"s ON i th CHANNEL	OUTPUT OF LOGIC CIRCUIT 205_i	NUMBER OF j th OUTPUTS OF ALL CROSS-CONNECTS THAT ARE "1"s	OUTPUT OF LOGIC CIRCUIT 211_j
①	ODD	"1"	ODD	"0"
②	EVEN	"0"	EVEN	"0"

(b) WHEN MALFUNCTION OCCURS

	NUMBER OF "1"s ON i th CHANNEL	OUTPUT OF LOGIC CIRCUIT 205_i	NUMBER OF j th OUTPUTS OF ALL CROSS-CONNECTS THAT ARE "1"s	OUTPUT OF LOGIC CIRCUIT 211_j
③	ODD	"1"	ODD	"0"
④	EVEN	"0"	EVEN	"0"

FIG. 4

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FIG. 5

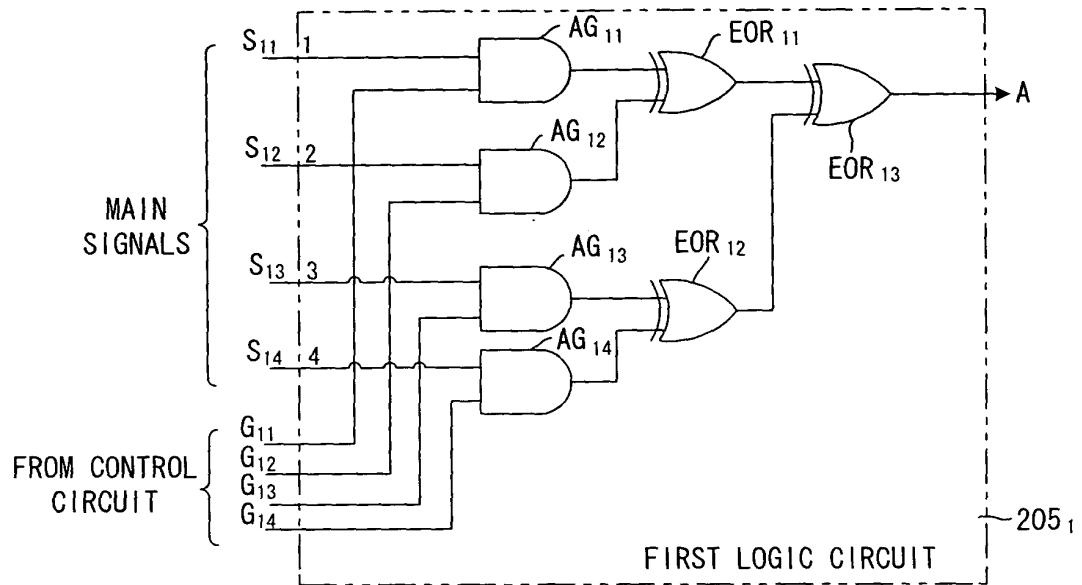


FIG. 6

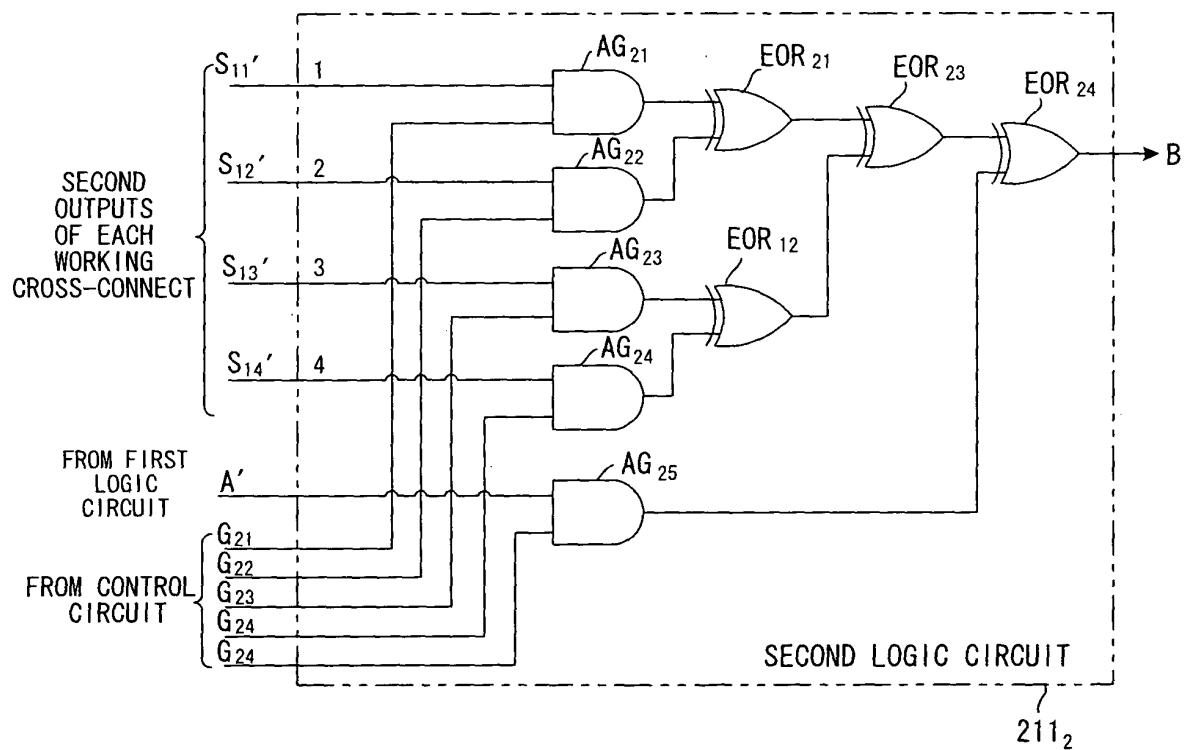


FIG. 7

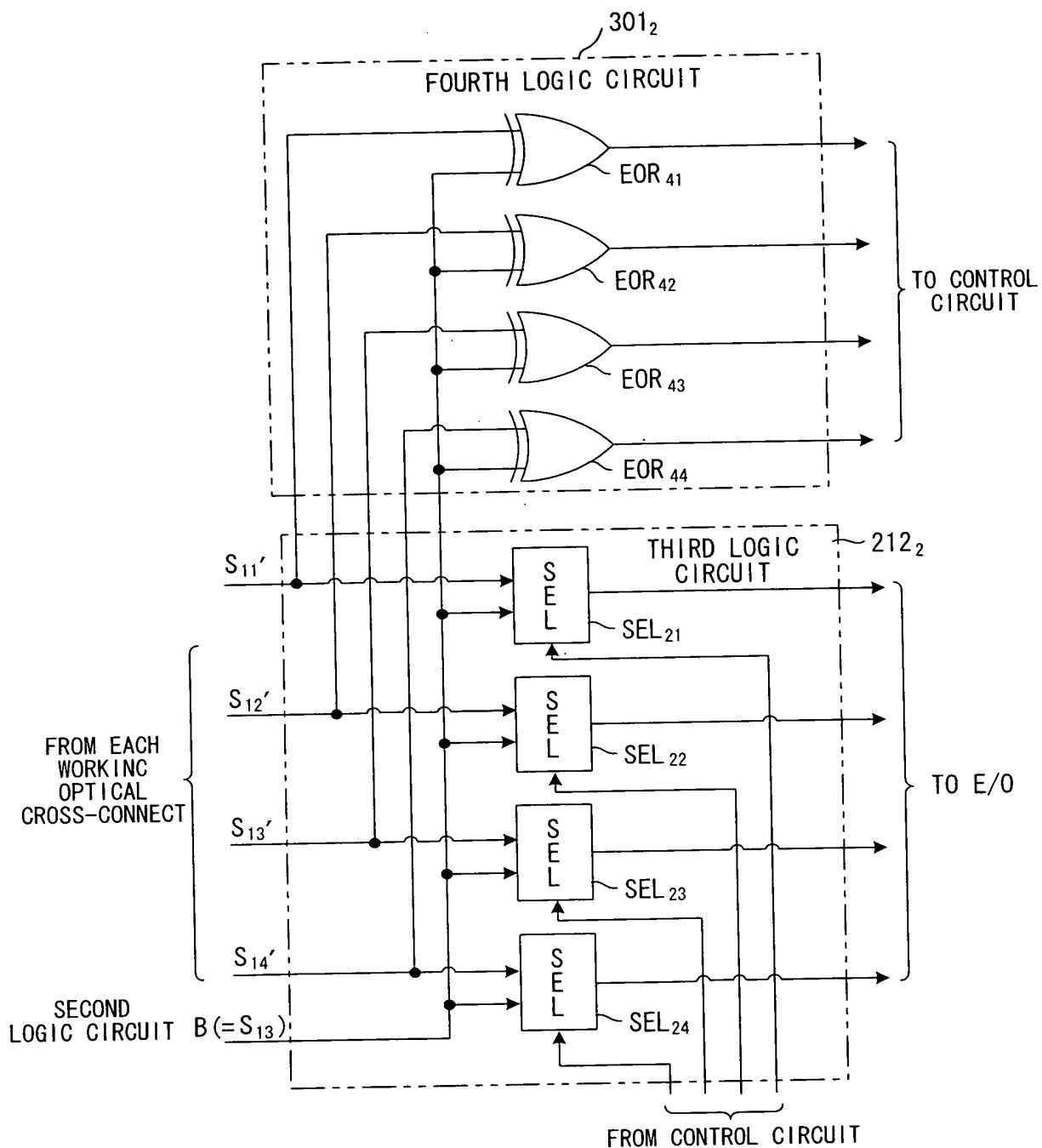


FIG. 8

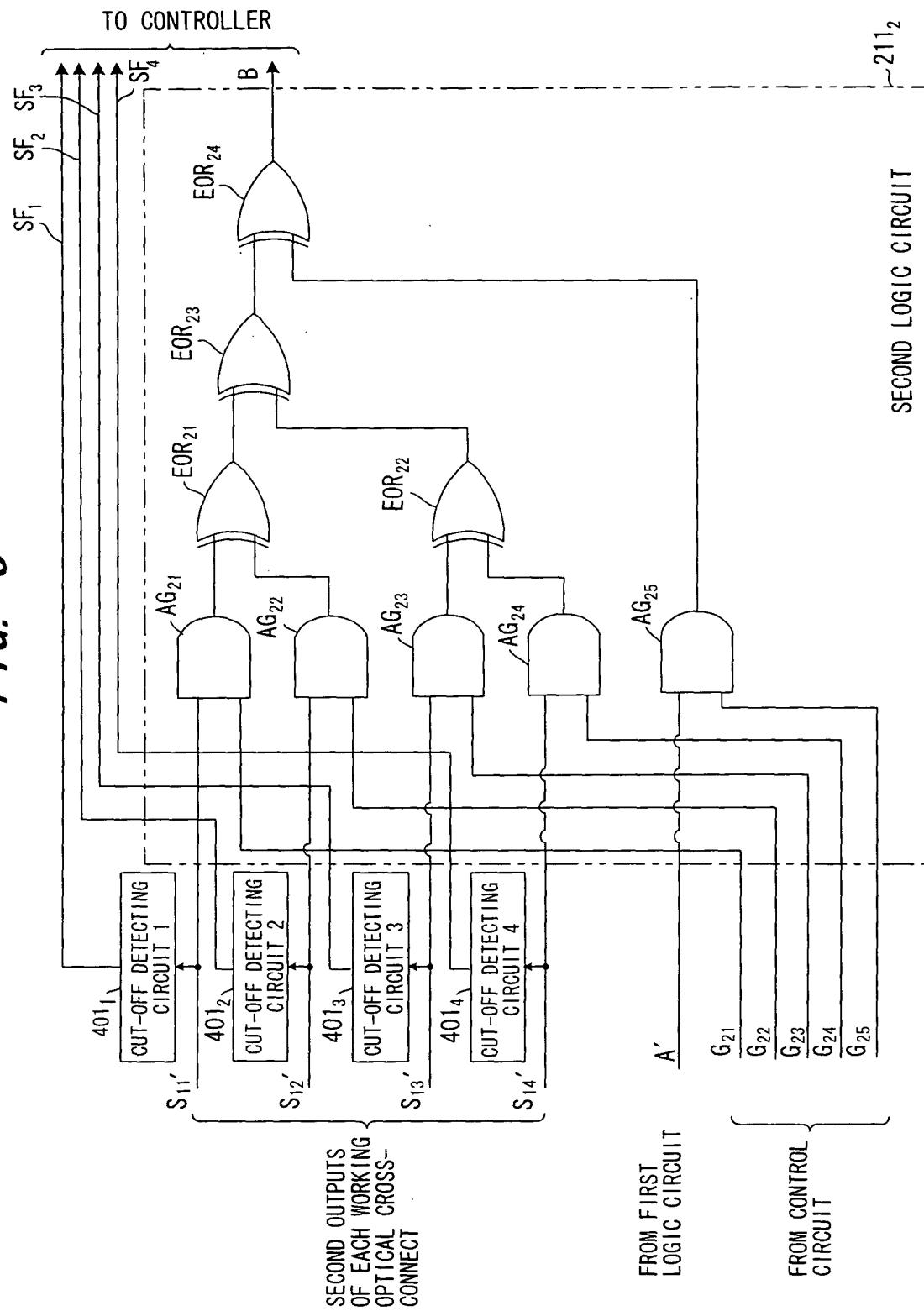


FIG. 9

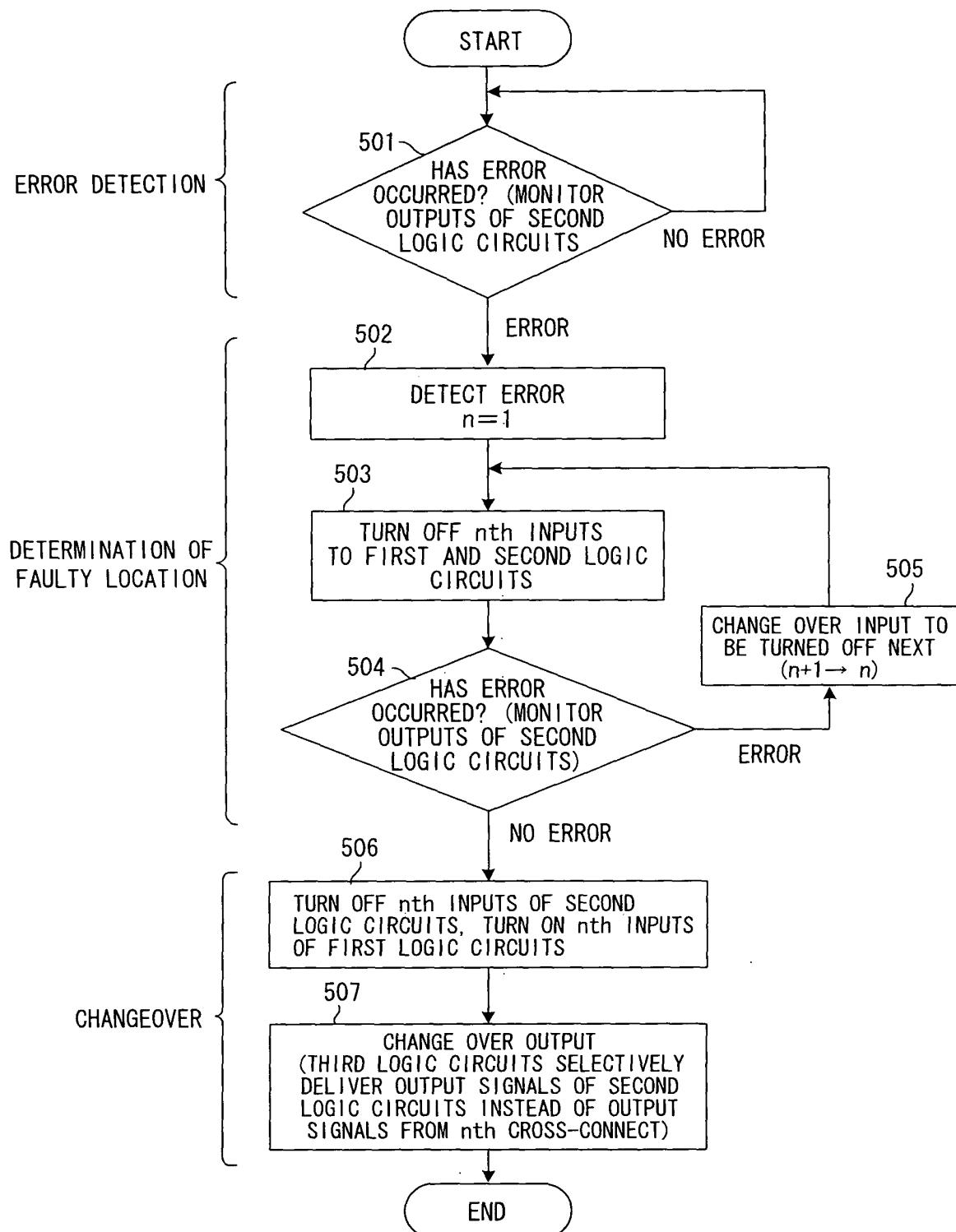


FIG. 10

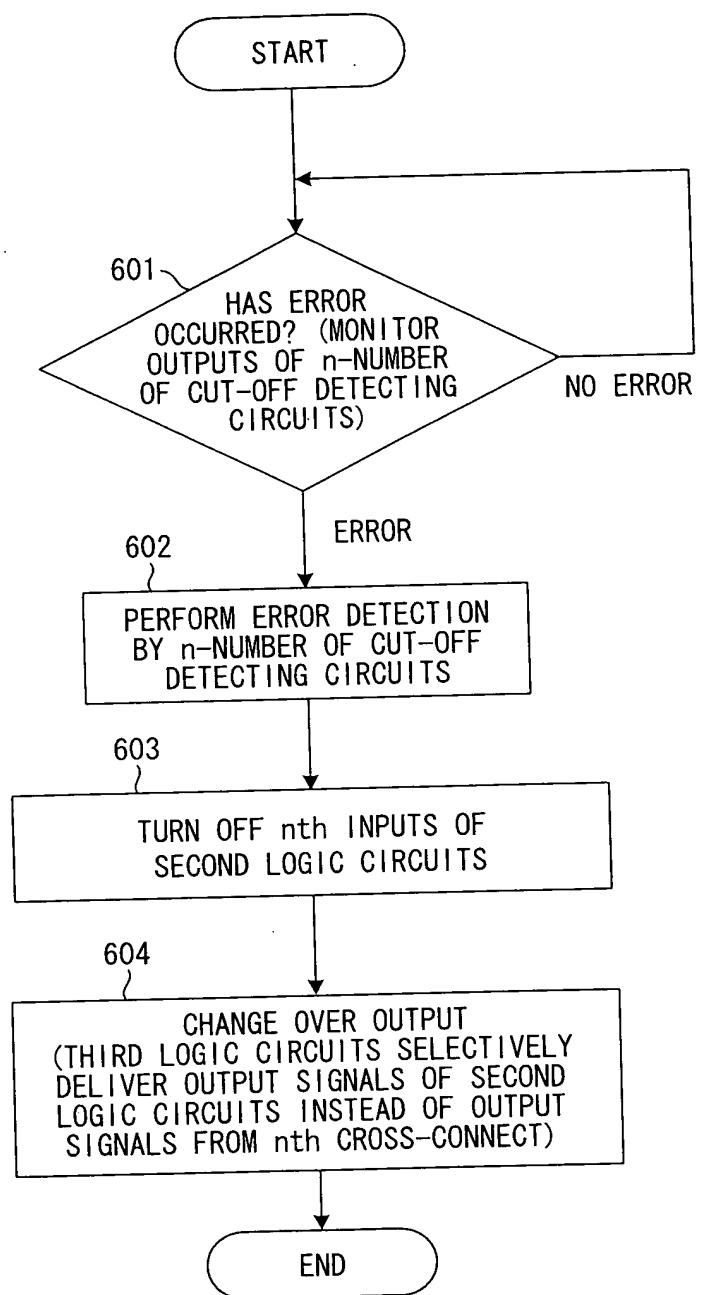


FIG. 11

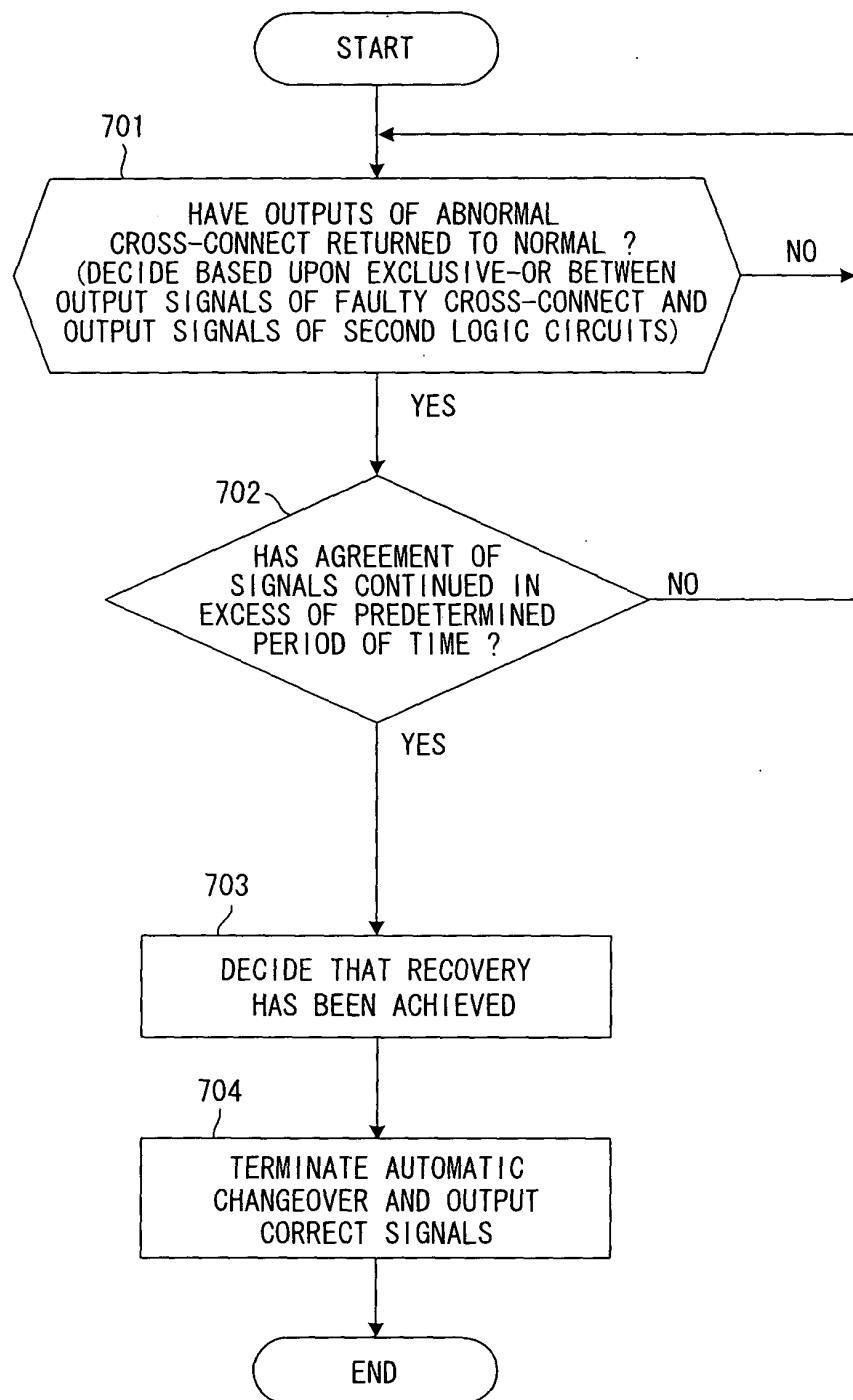


FIG. 12

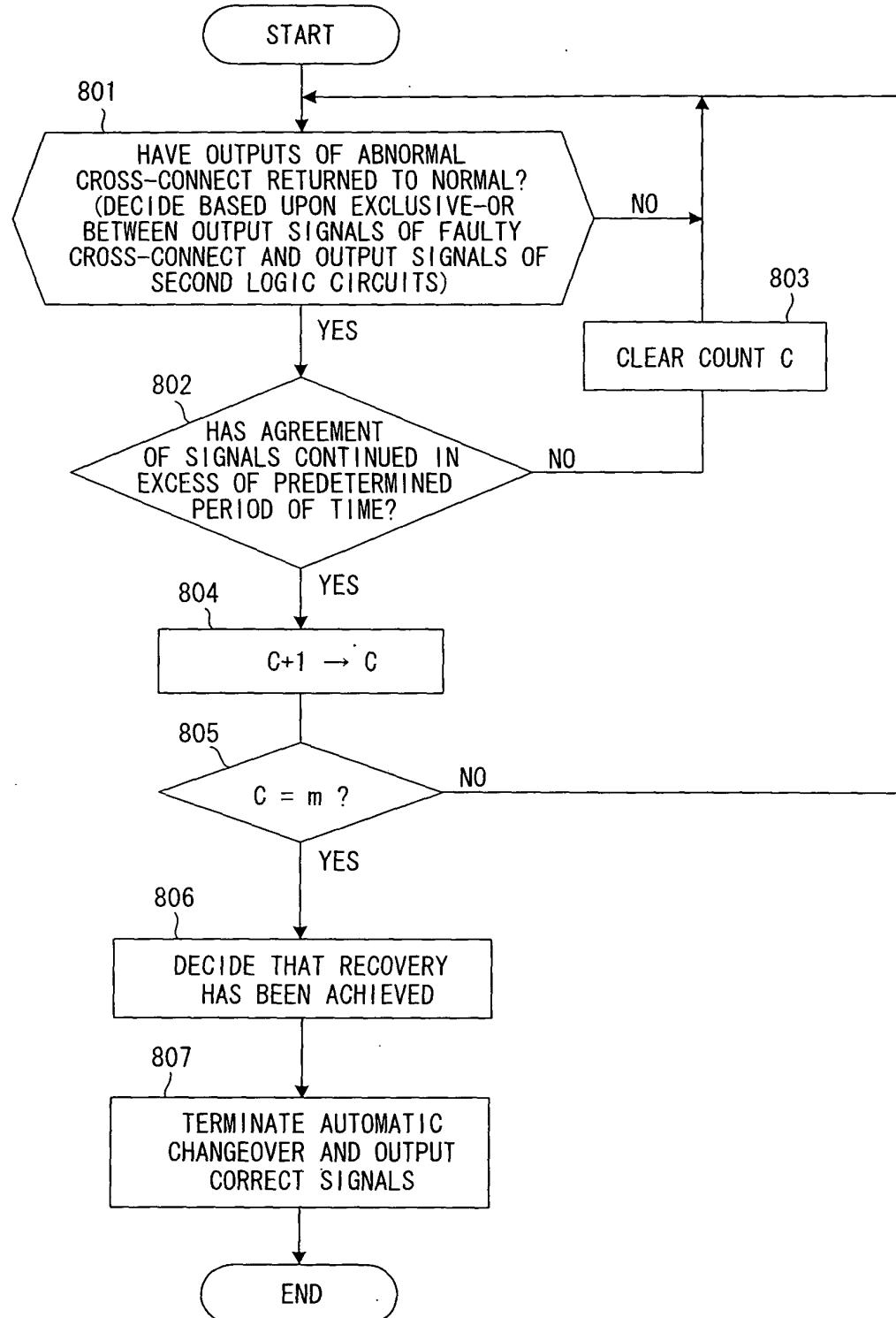


FIG. 13

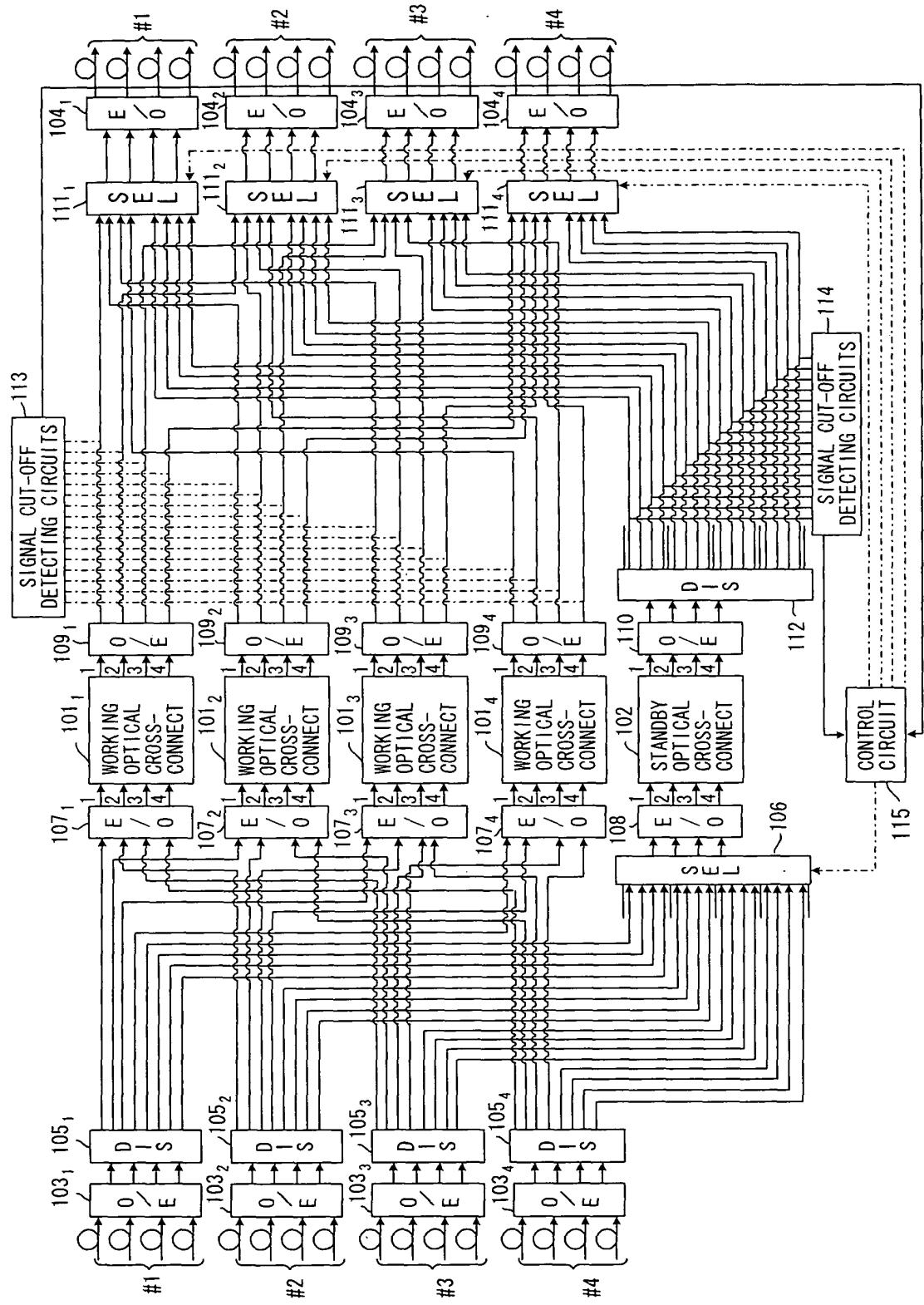


FIG. 14

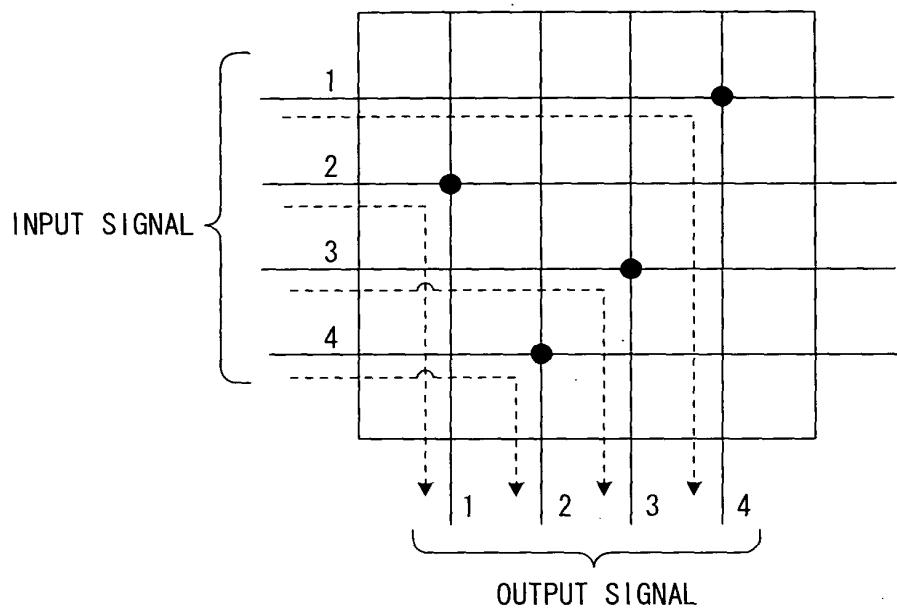


FIG. 15

